

[File 347] JAPIO Dec 1976-2007/Mar(Updated 070809)

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[File 350] Derwent WPIX 1963-2007/UD=200752

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Set Items Description

S1 52 S (STEINER(1W)(TREE? ? OR NODE OR POINT? ? OR VERTICE? ?)) OR (S()STEINER(1W)(TREE? ? OR NODE OR POINT? ? OR VERTICE? ?)) OR NASH()WILLIAMS()TUTTE(2N)THEOREM? ? OR ((NASH(1W)WILLIAMS(1W)TUTTE)(2N)THEOREM? ?)

S2 0 S ((STEINER(1W)TREE? ?)(5N)(SET OR SUBSET OR SUB()SET))(10N)(NODE OR STRUCTURE? ?)

S3 8 S ((STEINER(1W)TREE? ?)(10N)(CLIENT? ? OR ENTIT??? OR NODE))

S4 15 S S1(20N)(CREAT??? OR GENERAT???)

S5 5 S (S1(10N)(MERG??? OR PACK??? OR TOGETHER OR MIX??? OR BLEND??? OR BINDING OR BOUND OR COMBIN??? OR CONSOLIDAT??? OR COMPRESS??? OR CONDENS??? OR JOIN?????))

S6 11 S (S1 AND (MERG??? OR PACK??? OR TOGETHER OR MIX??? OR BLEND??? OR BINDING OR BOUND OR COMBIN??? OR CONSOLIDAT??? OR COMPRESS??? OR CONDENS??? OR JOIN?????))

S7 1 S S6 AND (AU=(JAIN, K? OR JAIN K?))

S8 1 S S6 AND (AU=(MAHDIAN, M? OR MAHDIAN M?))

S9 1 S S6 AND (AU=(SALAVATIPOUR, M? OR SALAVATIPOUR M?))

S10 1 S S7 AND S8 AND S9

Higher relevance

d

Subject summary6/3,K/1 (Item 1 from file: 350) [Links](#)

Derwent WPIX

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0016672134 *Drawing available*

WPI Acc no: 2007-387219/200736

XRPX Acc No: N2007-290264

Providing method for broadcast service involves broadcasting portion of broadcast services over each Steiner trees where all of broadcast services are broadcasted over multiple Steiner trees

Patent Assignee: ALCATEL (COGE); ALCATEL LUCENT (COGE); CIT-ALCATEL (CITC)

Inventor: BOU-DIAB B S; CUERVO F; RAAHEMI B

Patent Family (3 patents, 115 countries)

Patent Number	Kind	Date	Application Number	Kind	Date	Update	Type
US 20070047556	A1	20070301	US 2005212661	A	20050829	200736	B
WO 2007039827	A2	20070412	WO 20061B3672	A	20060828	200736	E
CN 1925449	A	20070307	CN 200610126564	A	20060828	200746	E

Priority Applications (no., kind, date): US 2005212661 A 20050829

Patent Details

Patent Number	Kind	Lan	Pgs	Draw	Filing Notes
US 20070047556	A1	EN	9	3	
WO 2007039827	A2	EN			
National Designated States,Original	AE AG AL AM AT AU AZ BA BB BG BR BW BY BZ CA CH CN CO CR CU CZ DE DK DM DZ EC EE EG ES FI GB GD GE GH GM HN HR HU ID IL IN IS JP KE KG KM KN KP KR KZ LA LC LK LR LS LT LU LV LY MA MD MG MK MN MW MX MY MZ NA NG NI NO NZ OM PG PH PL PT RO RS RU SC SD SE SG SK SL SM SV SY TJ TM TN TR TT TZ UA UG US UZ VC VN ZA ZM ZW				
Regional Designated States,Original	AT BE BG BW CH CY CZ DE DK EA EE ES FI FR GB GH GM GR HU IE IS IT KE LS LT LU LV MC MW MZ NA NL OA PL PT RO SD SE SI SK SL SZ TR TZ UG ZM ZW				

...broadcast services over each Steiner trees where all of broadcast services are broadcasted over multiple Steiner trees Alerting Abstract ...broadcasted over each Steiner trees where all of the broadcast services are broadcasted over multiple Steiner trees. ...wireless (PW). Ensures that every destination within the VPLS will be reached by the broadcast packet.DESCRIPTION OF DRAWINGS - The figure shows the disjoint Steiner tree with dual homing at respective provider edges (PE). Original Publication Data by Authority...Original Abstracts: on the faulty tree are switched to the other tree using split horizon bridging. Each Steiner tree can also be realized using point-to-multipoint LSPs which is fully protected by a... on the faulty tree are switched to the other tree using split horizon bridging. Each Steiner tree can also be realized using point-to- multipoint LSPs which is fully protected by a... Claims: routers in the VPLS network, the method comprising the steps: a) calculating a plurality of Steiner trees, each such tree being sourced by a respective source PE router and including each destination PE router, the plurality of Steiner trees being disjoint from one another with respect to at least the core P routers in the VPLS network; b) provisioning each of the Steiner trees with enough bandwidth to carry all of the broadcast services; and c) broadcasting a portion of the broadcast services over each of the Steiner trees, whereby all of the broadcast services are broadcasted over the plurality of Steiner trees.>

6/3,K/2 (Item 2 from file: 350) [Links](#)

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0016336798 *Drawing available*

WPI Acc no: 2007-052967/200706

XRPX Acc No: N2007-036557

Automatic routing method for interconnects of integrated circuit involves determining current requirement and track width of each track segment

Patent Assignee: PULSIC LTD (PULS-N)

Inventor: BALSDON G; BIRCH J; PARKER T; SATO F; WALLER M; WILLIAMS M

Patent Family (1 patents, 1 countries)

Patent Number	Kind	Date	Application Number	Kind	Date	Update	Type
US 7131096	B1	20061031	US 2004709843	A	20040601	200706	B

Priority Applications (no., kind, date): US 2004709843 A 20040601

Patent Details

Patent Number	Kind	Lan	Pgs	Draw	Filing Notes
US 7131096	B1	EN	17	7	

...of a circuit design and several pins (403) having an associated current value for routing together using a net are

provided. The current requirement of each track segment (411,413,406... Original Publication Data by Authority...Original Abstracts:will determine the current requirements for each net. In an implementation, the technique forms a Steiner tree for a net, and routes using the Steiner tree. The technique widens nets having greater current requirements; adjacent wiring may be pushed aside to... ..Claims:a circuit design;providing a plurality of pins of the gridless layout to be routed together using a net, each pin having a current value associated with it;using at least...

6/3,K/3 (Item 3 from file: 350) [Links](#)

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0015718522 Drawing available

WPI Acc no: 2006-281751/200629

Related WPI Acc No: 2003-419059

XRPX Acc No: N2006-240351

Dynamic directory of degree of freedom data for elements in non-conformal mixed-element mesh, comprises respective degree of freedom value stored for each element is current value for proceeding element subdivision

Patent Assignee: INT BUSINESS MACHINES CORP (IBMC)

Inventor: FISCHER S E; JOHNSON J B; YOUNG R W

Patent Family (1 patents, 1 countries)

Patent Number	Kind	Date	Application Number	Kind	Date	Update	Type
US 20060071931	A1	20060406	US 2001942419	A	20010830	200629	B
			US 2005294550	A	20051206		

Priority Applications (no., kind, date): US 2001942419 A 20010830; US 2005294550 A 20051206

Patent Details

Patent Number	Kind	Lan	Pgs	Draw	Filing Notes
US 20060071931	A1	EN	15	7	Continuation of application US 2001942419

Dynamic directory of degree of freedom data for elements in non-conformal mixed-element mesh, comprises respective degree of freedom value stored for each element is current value... Alerting Abstract ... USE - For elements such as prism, hexahedra, tetrahedra in non-conformal three-dimensional (3D) mixed-element mesh for designing pipelines for drainage system, wiring building, setting up network within office... .. ADVANTAGE - Provides tetrahedralized output by reducing number of Steiner points.Title Terms .../Index Terms/Additional Words: MIX;

6/3,K/4 (Item 4 from file: 350) [Links](#)

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0015074745 Drawing available

WPI Acc no: 2005-424184/200543

XRPX Acc No: N2005-344291

Steiner trees packing method for network data routing, generates Steiner trees and paths from undirected graph of vertices, and merges trees and paths to produce linked and edge-disjoint S-Steiner trees

Patent Assignee: JAIN K (JAIN-I); MAHDIAN M (MAHD-I); SALAVATIPOUR M R (SALA-I)

Inventor: JAIN K; MAHDIAN M; SALAVATIPOUR M R

Patent Family (1 patents, 1 countries)

Patent Number	Kind	Date	Application Number	Kind	Date	Update	Type
US 20050111386	A1	20050526	US 2003719182	A	20031120	200543	B

Priority Applications (no., kind, date): US 2003719182 A 20031120

Patent Details

Patent Number	Kind	Lan	Pgs	Draw	Filing Notes
US 20050111386	A1	EN	16	5	

Steiner trees packing method for network data routing, generates Steiner trees and paths from undirected graph of vertices, and merges trees and paths to produce linked and edge-disjoint S-Steiner trees Original
Titles:Packing steiner trees Alerting Abstract ...NOVELTY - The method involves generating a set of Steiner trees and paths from an undirected graph of vertices representing terminal and Steiner nodes. A determination... ..whether one path shares an edge with the tree. The trees and the paths are merged to produce linked and edge-disjoint S- Steiner trees such that there is preset number of edge-disjoint trees for the subset if a... .. a computer-readable medium comprising computer executable instructions for packing Steiner trees a computing device comprising a memory with computer instructions for generating a set of Steiner trees and paths from an undirected graph of vertices... ..
ADVANTAGE - The method merges the Steiner trees and the paths to optimize data throughput and to provide end-users with a large number of data viewing options... ..
DESCRIPTION OF DRAWINGS - The drawing shows a flowchart of a procedure to locate maximum sized Steiner trees to connect to a same set of terminal vertices.**Title Terms** .../Index Terms/Additional Words: PACK;MERGE; Original Publication Data by AuthorityOriginal
Abstracts:Systems and methods for packing Steiner trees are described. In one aspect, a set of Steiner trees and paths are generated from an undirected graph of vertices representing terminal and Steiner nodes. The Steiner trees and the paths are merged to produce linked and edge disjoint S-Steiner trees. If a subset S of the vertices is edge

connected, then at minimum there are substantially $\alpha|S|^k$ edge-disjoint Steiner trees for S , wherein α is a sequence that tends to an asymptotic approximation factor of $|S|/4$ as S tends to... **Claims:1.** A method comprising: generating a set of Steiner trees and paths from an undirected graph of vertices representing terminal and Steiner nodes; and merging the Steiner trees and the paths to produce linked and edge-disjoint S -Steiner trees such that if a subset S of the vertices is k edge-connected, then there are $\alpha|S|^k$ edge-disjoint Steiner trees for S , where α is at minimum a sequence that tends to an asymptotic approximation factor of $|S|/4$ as s tends to infinity.

6/3,K/5 (Item 5 from file: 350) [Links](#)

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0014906790

WPI Acc no: 2005-254435/200527

XRPX Acc No: N2005-209390

Steiner tree method for $O(n \log n)$ under 4-geometry

Patent Assignee: UNIV QINGHUA (UYQI)

Inventor: HONG X; JING T; YANG Y

Patent Family (1 patents, 1 countries)

Patent Number	Kind	Date	Application Number	Kind	Date	Update	Type
CN 1558350	A	20041229	CN 200410000098	A	20040114	200527	B

Priority Applications (no., kind, date): CN 200410000098 A 20040114

Patent Details

Patent Number	Kind	Lan	Pgs	Draw	Filing Notes
CN 1558350	A	ZH		0	

Steiner tree method for $O(n \log n)$ under 4-geometry ...NOVELTY - The Steiner tree method under $O(n \log n)$ of 4-geometric structure belongs to the field of IC CAD... 4-geometric structure edge substituting method and three-point group method are designed separately and combined to the minimum spanning tree to obtain Steiner tree. The present invention has less complexity and higher Steiner tree constituting efficiency compared with $O(n^2)$ method.

6/3,K/6 (Item 6 from file: 350) [Links](#)

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0014853474 *Drawing available*

WPI Acc no: 2005-201178/200521

Related WPI Acc No: 2003-428638; 2004-302940; 2004-591207; 2005-036913; 2005-201148; 2005-201149; 2005-201150; 2005-281850; 2005-281925; 2005-329593; 2005-353140; 2005-381297; 2005-381298; 2005-381402; 2005-393668; 2005-393669; 2005-414879; 2005-423369; 2005-432117; 2005-503980; 2005-519209; 2005-561052; 2005-580025; 2005-580026; 2005-603212; 2005-646711; 2005-673360; 2005-681888; 2005-681889; 2005-732335; 2005-732336; 2005-732337; 2005-766265; 2005-766266; 2006-016793; 2006-036171; 2006-053723; 2006-097829; 2006-170629; 2006-228609; 2006-260034; 2006-303933; 2006-349898; 2006-371524; 2006-412980; 2006-490565; 2006-490700; 2006-490701; 2006-510140; 2006-547519; 2006-600816; 2006-645744; 2006-706214; 2006-744027; 2006-744188; 2007-081923; 2007-108498; 2007-251723

XRPX Acc No: N2005-165516

Integrated circuit layout has set of vias each traversing two or more layers with diamond shaped contact on each layer for terminating ends of interconnect lines

Patent Assignee: CADENCE DESIGN SYSTEMS INC (CADE-N)

Inventor: CALDWELL A; TEIG S

Patent Family (1 patents, 1 countries)

Patent Number	Kind	Date	Application Number	Kind	Date	Update	Type
US 6859916	B1	20050222	US 2001295735	P	20010603	200521	B
			US 2001298146	P	20010612		
			US 2002351459	P	20020112		
			US 200262995	A	20020131		

Priority Applications (no., kind, date): US 2001295735 P 20010603; US 2001298146 P 20010612; US 2002351459 P 20020112; US 200262995 A 20020131

Patent Details

Patent Number	Kind	Lan	Pgs	Draw	Filing Notes
US 6859916	B1	EN	86	83	Related to Provisional US 2001295735
					Related to Provisional US 2001298146
					Related to Provisional US 2002351459

Alerting Abstract ...DESCRIPTION OF DRAWINGS - The figure shows illustration of layout of two layers combined to produce a subregion after nodes have been defined at each subregion corner, shortest partial path after constraining segments of the interconnected path defining ends and hexagonal steiner node formed by half-hexagonal lines... 6905 octagonal steiner node Original Publication Data by Authority...Original Abstracts:the interconnect-line ends are partial

octagons, hexagons, and/or circles. Also, some embodiments provide Steiner points that are not rectangular. In some embodiments, the Steiner points are octagonal, hexagonal, or circles.

6/3,K/7 (Item 7 from file: 350) [Links](#)

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0013331650 *Drawing available*

WPI Acc no: 2003-419059/200339

Related WPI Acc No: 2006-281751

XRPX Acc No: N2003-334446

Dynamic degree-of-freedom data directory used in e.g. software testing/simulation, has non-conformal mixed-element mesh subdividable into tetrahedra, in which degree of freedom value is set for each element to perform element subdivision

Patent Assignee: INT BUSINESS MACHINES CORP (IBMC)

Inventor: FISCHER S E; JOHNSON J B; YOUNG R W

Patent Family (2 patents, 1 countries)

Patent Number	Kind	Date	Application Number	Kind	Date	Update	Type
US 20030046046	A1	20030306	US 2001942419	A	20010830	200339	B
US 7099805	B2	20060829	US 2001942419	A	20010830	200657	E

Priority Applications (no., kind, date): US 2001942419 A 20010830

Patent Details

Patent Number	Kind	Lan	Pgs	Draw	Filing Notes
US 20030046046	A1	EN	14	6	

...degree-of-freedom data directory used in e.g. software testing/simulation, has non-conformal mixed-element mesh subdividable into tetrahedra, in which degree of freedom value is set for each... **Original Titles:** Tetrahedralization of non-conformal three-dimensional mixed element meshes... Tetrahedralization of non-conformal three-dimensional mixed element meshes **Alerting Abstract** ...**NOVELTY** - The dynamic directory of degree of freedom data for elements in a non-conformal mixed-element mesh has elements subdividable into tetrahedra, having respective degree of freedom value for each... **ADVANTAGE** - Achieves reduction in the number of Steiner points needed to tetrahedralize a typical non-conformal mixed element mesh compared to the Albertelli technique, thereby providing both the speed and the convergence behavior of software which uses the resulting tetrahedral... **Title Terms** .../Index Terms/Additional Words: MIX; Original Publication Data by Authority**Original Abstracts:**Undesirable Steiner points in tetrahedralized meshes may be minimized by tetrahedralization processes that order element subdivision based on degree of freedom data... ahead, breadth-first-search subdivision, and other strategic subdivision techniques further minimizes the need for Steiner points. ... Undesirable Steiner points in tetrahedralized meshes may be minimized by tetrahedralization processes that order element subdivision based on degree of freedom data for elements in the mesh... ahead, breadth-first-search subdivision, and other strategic subdivision techniques further minimizes the need for Steiner points. >...**Claims:**1. A dynamic directory of degree of freedom data for elements in a non-conformal mixed-element mesh comprising elements subdividable into tetrahedra, comprising: a respective degree of freedom value for each element, wherein the degree of freedom value is... claim:1. A tetrahedralizing filter, comprising: a receiver for data defined on a non-conformal mixed element mesh comprising elements subdividable into tetrahedra,a processor for the mesh data, wherein the processor dynamically associates individual to-be-subdivided elements in the mesh with a degree of freedom value in an element-by-element degree of...

6/3,K/8 (Item 8 from file: 350) [Links](#)

Derwent WPIX

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0012962808 *Drawing available*

WPI Acc no: 2003-039915/200303

XRPX Acc No: N2003-031270

Buffered Steiner tree determination method for complex integrated circuit involves generating local and global wiring patterns in Steiner tree format for each cluster of sinks

Patent Assignee: INT BUSINESS MACHINES CORP (IBMC)

Inventor: ALPERT C J; GANDHAM R G; HU J; QUAY S T; SULLIVAN A J

Patent Family (2 patents, 1 countries)

Patent Number	Kind	Date	Application Number	Kind	Date	Update	Type
US 20020133799	A1	20020919	US 2001810075	A	20010315	200303	B
US 6591411	B2	20030708	US 2001810075	A	20010315	200353	E

Priority Applications (no., kind, date): US 2001810075 A 20010315

Patent Details

Patent Number	Kind	Lan	Pgs	Draw	Filing Notes
US 20020133799	A1	EN	17	8	

Buffered Steiner tree determination method for complex integrated circuit involves generating local and global wiring patterns in Steiner tree format for each cluster of sinks Original Titles: Apparatus and method for determining buffered steiner trees for complex circuits... Apparatus and method for determining buffered steiner trees for complex circuits **Alerting Abstract** ... circuit having similar characteristics are clustered. Local and global wiring patterns are sequentially generated in Steiner tree format for each cluster. A buffer is inserted to the circuit based on the local... USE - For determining buffered Steiner tree in complex integrated circuits. Original Publication Data by Authority Original Abstracts: An apparatus and method for determining buffered Steiner trees for complex circuits is provided. The apparatus and method first clusters sinks with similar characteristics such as criticality, polarity... ones and non-critical sinks from critical ones. The present invention then constructs low-level Steiner trees over each of these clusters. Finally, a top-level timing driven Steiner tree is computed where each cluster is treated as a sink. The top-level tree is then merged with the low-level trees to yield a solution for the entire net... An apparatus and method for determining buffered Steiner trees for complex circuits is provided. The apparatus and method first clusters sinks with similar characteristics such as criticality, polarity and distance. The purpose of... ones and non-critical sinks from critical ones. The present invention then constructs low-level Steiner trees over each of these clusters. Finally, a top-level timing driven Steiner tree is computed where each cluster is treated as a sink. The top-level tree is then merged with the low-level trees to yield a solution for the entire net... Claims: of the sinks into one or more clusters; generating a local wiring pattern in a Steiner tree format for each cluster; generating a global wiring pattern to each of the one or more clusters in a Steiner tree format; and performing buffer insertion based on the local wiring pattern and global wiring pattern.... of the sinks into one or more clusters; generating a local wiring pattern in a Steiner tree format for each cluster; generating a global wiring pattern to each of the one or more clusters in a Steiner tree format; and performing buffer insertion based on the local wiring pattern and global wiring pattern.

6/3,K/9 (Item 9 from file: 350) [Links](#)

Derwent WPIX

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0012639270 Drawing available

WPI Acc no: 2002-488354/200252

XRPX Acc No: N2002-385950

Steiner tree generation method for sub-microcircuit design, involves determining solution values for Boolean network function from binary decision diagram

Patent Assignee: SUN MICROSYSTEMS INC (SUNM)

Inventor: LAM W; XING Z

Patent Family (1 patents, 1 countries)

Patent Number	Kind	Date	Application Number	Kind	Date	Update	Type
US 6389376	B1	20020514	US 1999360339	A	19990726	200252	B

Priority Applications (no., kind, date): US 1999360339 A 19990726

Patent Details

Patent Number	Kind	Lan	Pgs	Draw	Filing Notes
US 6389376	B1	EN	13	10	

Steiner tree generation method for sub-microcircuit design, involves determining solution values for Boolean network function from... Original Titles: Method and apparatus for generating n-segment steiner trees. **Alerting Abstract** ... A binary decision diagram is determined for the Boolean network function. A solution for a Steiner tree is determined by determining solution values for the Boolean network function from the binary decision... Computer program product comprising recorded medium storing instructions for generating Steiner tree; Computer system; and Steiner tree generation system. ... USE - For generating n-segment Steiner tree representing minimal manhattan-space distance connections, for design of sub-microcircuits, in computer system (claimed... ADVANTAGE - Yields all n-segment Steiner trees interconnecting the desired points, avoiding obstacles. Provides an optimal Steiner tree... DESCRIPTION OF DRAWINGS - The figure shows the Steiner tree solution. Original Publication Data by Authority Original Abstracts: The invention is a method and apparatus for generating one or more Steiner trees representing a connection of at least two points. In accordance with an embodiment of the... points to be connected, the interconnects having no greater than "n" segments and arranged to join at a joint. In accordance with this embodiment, the solution values yielded by the BDD path(s) comprise the coordinates of the interconnect joint. Claims: In a computer system, a method of generating a Steiner tree for connecting at least two points comprising: generating a Boolean network function representing a network of interconnects... a binary decision diagram for said Boolean network function; and determining a solution for said Steiner tree by determining solution values for said Boolean network function from said binary decision diagram.

6/3,K/10 (Item 10 from file: 350) [Links](#)

Derwent WPIX

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0010388590 Drawing available

WPI Acc no: 2000-257272/200023

XRPX Acc No: N2000-191308

Multicast Stienner backbone (MSBone) system for multicast routing in a telecommunications network, involves one host storing information about all hosts so that a Stienner spanning tree can be created for the multicast

routers

Patent Assignee: LUCENT TECHNOLOGIES INC (LUCE)

Inventor: AGGARWAL S; PAUL S

Patent Family (3 patents, 2 countries)

Patent Number	Kind	Date	Application Number	Kind	Date	Update	Type
CA 2244333	A	19990226	CA 2244333	A	19980730	200023	B
US 6154463	A	20001128	US 1997917344	A	19970826	200063	E
CA 2244333	C	20030415				200330	E

Priority Applications (no., kind, date): US 1997917344 A 19970826

Patent Details

Patent Number	Kind	Lan	Pgs	Draw	Filing Notes
CA 2244333	A	EN	75	17	
CA 2244333	C	EN			

Original Publication Data by Authority...**Original Abstracts:**for multicast conferencing and online discussion groups using a periodically determined a close-to-optimal Steiner spanning tree. Both the system and method are suitable for stationary and/or mobile group members. ...**Claims:**group, the multicast router connected to the selected host determining a current close-to-optimal Steiner spanning tree formed from multicast routers that are connected to hosts that are members of the multicast group, members of the multicast group... .. multicast information to other members of the multicast group over the current close-to-optimal Steiner spanning tree; anda multicast sessions directory running in the domain and accessible to possible participants wishing to join a multicast conferencing/discussion group session.

6/3,K/11 (Item 11 from file: 350) [Links](#)

Derwent WPIX

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0008466460 Drawing available

WPI Acc no: 1997-471102/199743

XRPX Acc No: N1997-392988

Computer implemented method for routing interconnections between terminals of electrical components mounted in substrate, board or package - routing interconnections between number of circuits among layers of multilayer substrate by generating multiple candidate routes for each connection and analysing each route for compatibility

Patent Assignee: ARIZONA BOARD OF REGENTS (ARIZ-N); UNIV ARIZONA STATE (UYAR-N)

Inventor: CAROTHERS J D; LI D

Patent Family (4 patents, 73 countries)

Patent Number	Kind	Date	Application Number	Kind	Date	Update	Type
WO 1997034316	A2	19970918	WO 1997US4116	A	19970312	199743	B
AU 199723279	A	19971001	AU 199723279	A	19970312	199805	E
WO 1997034316	A3	19971023				199815	E
US 6353918	B1	20020305	US 199613481	P	19960315	200224	E
			WO 1997US4116	A	19970312		
			US 1999142793	A	19990805		

Priority Applications (no., kind, date): US 1999142793 A 19990805; US 199613481 P 19960315

Patent Details

Patent Number	Kind	Lan	Pgs	Draw	Filing Notes
WO 1997034316	A2	EN	96	37	
National Designated States,Original	AL AM AT AU AZ BA BB BG BR BY CA CH CN CU CZ DE DK EE ES FI GB GE GH HU IL IS JP KE KG KP KR KZ LC LK LR LS LT LU LV MD MG MK MN MW MX NO NZ PL PT RO RU SD SE SG SI SK TJ TM TR TT UA UG US UZ VN YU				
Regional Designated States,Original	AT BE CH DE DK EA ES FI FR GB GH GR IE IT KE LS LU MC MW NL OA PT SD SE SZ UG				
AU 199723279	A	EN			Based on OPI patent WO 1997034316
WO 1997034316	A3	EN			
US 6353918	B1	EN			Related to Provisional US 199613481
					PCT Application WO 1997US4116
					Based on OPI patent WO 1997034316

...implemented method for routing interconnections between terminals of electrical components mounted in substrate, board or package - Title Terms .../Index Terms/Additional Words: PACKAGE; Original Publication Data by Authority...Claims:least one multi-terminal net into (n-1) number of two-terminal nets; andinserting Steiner points into at least one route corresponding to a net.

[File 348] EUROPEAN PATENTS 1978-2007/ 200731

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*File 348: For important information about IPCR/8 and forthcoming changes to the IC= index, see HELP NEWSIPCR.

[File 349] PCT FULLTEXT 1979-2007/UB=20070809UT=20070802

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*File 349: For important information about IPCR/8 and forthcoming changes to the IC= index, see HELP NEWSIPCR.

Set Items Description

S1 65 S (STEINER(1W)(TREE? ? OR NODE OR POINT? ? OR VERTICE? ?)) OR (S()STEINER(1W)(TREE? ? OR NODE OR POINT? ? OR VERTICE? ?)) OR NASH()WILLIAMS()TUTTE(2N)THEOR????? OR ((NASH(1W)WILLIAMS(1W)TUTTE)(2N)THEOR?????)

S2 10868 S ((STEINER(1W)TREE? ? OR TREE? ? OR NODE OR POINT? ? OR VERTICE? ?)(5N)(SET OR SUBSET OR SUB()SET))(10N)(NODE OR STRUCTURE? ?)

S3 111657 S ((STEINER(1W)TREE? ? OR TREE? ? OR NODE OR POINT? ? OR VERTICE? ?)(10N)(CLIENT? ? OR ENTIT??? OR NODE))

S4 28 S S1(20N)(CREAT??? OR GENERAT???)

S5 12 S (S1(10N)(MERG??? OR PACK??? OR TOGETHER OR MIX??? OR BLEND??? OR BINDING OR BOUND OR COMBIN??? OR CONSOLIDAT??? OR COMPRESS??? OR CONDENS??? OR JOIN?????))

Higher relevance

d

Subject summary

? t/3,k/all

5/3K/1 (Item 1 from file: 348) [Links](#)

EUROPEAN PATENTS

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02142812

Architecture for provisioning broadcast services over managed multicast virtual private LAN trees

Architektur zum Bereitstellen von Broadcastdiensten durch verwaltete Mehrfachadressenbaume in einem virtuellen privaten LAN

Architecture pour fournir des services de diffusion sur des arbres d'emission multidestinatoires geres dans un lan prive virtuel

Patent Assignee:● **ALCATEL; (201871)**

54, rue la Boetie; 75008 Paris; (FR)

(Applicant designated States: all)

Inventor:● **Bou-Diab, Bashar Said**

87 Fifth Avenue; OttawaOntarioK1S 2M3; (CA)

● **Raahemi, Bijan**

139 Riverdale Avenue; OttawaOntarioK1S 1R1; (CA)

Legal Representative:● **Hervouet, Sylvie et al (92321)**

Feraÿ Lenne Conseil, 39/41, avenue Aristide Briand; 92163 Anthony Cedex; (FR)

	Country	Number	Kind	Date	
Patent	EP	1713199	A1	20061018	(Basic)
Application	EP	2006300149		20060220	
Priorities	US	60465		20050218	

Designated States:

AT; BE; BG; CH; CY; CZ; DE; DK; EE; ES;

FI; FR; GB; GR; HU; IE; IS; IT; LI; LT;

LU; LV; MC; NL; PL; PT; RO; SE; SI; SK;

TR;

Extended Designated States:

AL; BA; HR; MK; YU;

IPC	Level	Value	Position	Status	Version	Action	Source	Office
H04L-0012/18	A	I	F	B	20060101	20060324	H	EP

Abstract Word Count: 161**NOTE:** 3**NOTE:** Figure number on first page: 3

Type	Pub. Date	Kind	Text
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Publication: English

Procedural: English

Application: English

Available Text	Language	Update	Word Count
CLAIMS A	(English)	200642	1859
SPEC A	(English)	200642	6688
Total Word Count (Document A) 8547			
Total Word Count (Document B) 0			
Total Word Count (All Documents) 8547			

Specification: ...tracking complete subscription information in respect of each channel is also required. Although employing a Steiner tree would guarantee minimal transport bandwidth / packet processing resource utilization, the bandwidth required to autonomously track and store, network wide resource utilization...Steiner multicast trees based on multicast group membership information. The computation of the minimum cost Steiner multicast tree at the NMS 322 also takes into account packet switching capacities at communications network nodes 310 and transport bandwidth reservations on interconnecting links 108...

5/3K/2 (Item 2 from file: 348) [Links](#)

EUROPEAN PATENTS

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01189022

Adaptive routing system and method for Qos packet networks

ADAPTIVES LEITWEGLENKUNGSSYSTEM UND -VERFAHREN FUR QOS-PAKETNETZE

SYSTEME ET PROCEDE ADAPTATIFS D'ACHEMINEMENT POUR RESEAUX DE PAQUETS A QUALITE DE

SERVICE GARANTIE

Patent Assignee:

• **LUCENT TECHNOLOGIES INC.;** (2143720)
600 Mountain Avenue; Murray Hill, New Jersey 07974-0636; (US)
(Applicant designated States: all)

Inventor:

• **Aukia, Petri**
Pikkumaent 12; 04600 Mantsala; (FI)

• **Lakshman, T.V.**
118 Victoria Drive; Eatontown, New Jersey 07724; (US)

• **Mitra, Debasis**
133 Summit Avenue, Apt. 43; Summit, New Jersey 07901; (US)

• **Kajamalai, G. Ramakrishnan**
146 Maple Avenue; Berkeley Heights, New Jersey 07922; (US)

• **Stiliadis, Dimitrios**
2 Brentwood Terrace; Middletown, New Jersey 07748; (US)

Legal Representative:

• **Watts, Christopher Malcolm Kelway, Dr. et al** (37392)
Lucent Technologies (UK) Ltd, 5 Mornington Road; Woodford Green Essex IG8 OTU; (GB)

	Country	Number	Kind	Date	
Patent	EP	1035751	A2	20000913	(Basic)
	EP	1035751	A3	20040707	
Application	EP	2000301552		20000228	
Priorities	US	266622		19990311	

Designated States:

AT; BE; CH; CY; DE; DK; ES; FI; FR; GB;
GR; IE; IT; LI; LU; MC; NL; PT; SE;

Extended Designated States:

AL; LT; LV; MK; RO; SI;

International Patent Class (V7): H04Q-011/04 Abstract Word Count: 223

NOTE: 2

NOTE: Figure number on first page: 2

Type	Pub. Date	Kind	Text
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Publication: English
Procedural: English
Application: English

Available Text	Language	Update	Word Count
CLAIMS A	(English)	200037	916
SPEC A	(English)	200037	13293
Total Word Count (Document A) 14209			
Total Word Count (Document B) 0			
Total Word Count (All Documents) 14209			

Specification: ...be thought of as routing over N single paths. In addition, mathematical tools, such as **Steiner path trees**, heuristic bin **packing**, randomized routing, or shortest path spanning trees, may be employed by the algorithms described herein...

5/3K/3 (Item 3 from file: 348) [Links](#)

EUROPEAN PATENTS

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01055951

METHOD FOR ALLOCATING RADIO TECHNICAL RESOURCES AND A DEVICE PROVIDED FOR RADIO RESOURCE MANAGEMENT

VERFAHREN ZUR ZUORDNUNG VON FUNKTECHNISCHEN RESSOURCEN UND EINRICHTUNG ZUM FUNKRESSOURCENMANAGEMENT

PROCEDE D'AFFECTATION DE RESSOURCES TECHNIQUES RADIO ET DISPOSITIF POUR LA GESTION DE RESSOURCES RADIO

Patent Assignee:

• **SIEMENS AKTIENGESELLSCHAFT;** (200520)
Wittelsbacherplatz 2; 80333 München; (DE)
(Proprietor designated states: all)

Inventor:

• **SCHINDLER, Jurgen**

Gottfried-Bohm-Ring 23; D-81369 Munchen; (DE)

	Country	Number	Kind	Date	
Patent	EP	1032987	A1	20000906	(Basic)
	EP	1032987	B1	20030409	
	WO	99026359		19990527	
Application	EP	98965069		19981112	
	WO	98DE3331		19981112	
Priorities	DE	19751110		19971118	

Designated States:

DE; FR; GB;

International Patent Class (V7): H04B-007/26; H04J-013/02; H04L-012/28; H04Q-007/36

NOTE: No A-document published by EPO

Type	Pub. Date	Kind	Text
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Publication: German

Procedural: German

Application: German

Available Text	Language	Update	Word Count
CLAIMS B	(English)	200315	656
CLAIMS B	(German)	200315	528
CLAIMS B	(French)	200315	726
SPEC B	(German)	200315	2498
Total Word Count (Document A) 0			
Total Word Count (Document B) 4408			
Total Word Count (All Documents) 4408			

Specification: ...Zuordnung funkt technischer Ressourcen an den Ressourcenverteilungswert Formanpassungs-Algorithmen verwendet. Besonders vorteilhaft sind dabei die sogenannten "Packing Steiner Trees"- Algorithmen für sehr genaue Lösungen oder die "Hybrid First Fit Packing"-Algorithmen, die einfach zu implementieren sind und Ergebnisse mit geringem Zeitverzug hervorbringen.

Die Erfindung soll ...zur Formanpassung der Ist-Verteilung an den gewünschten Ressourcenverteilungswert S1 benutzt. Vorteilhafte Verfahren sind die "Packing Steiner Trees"- oder die "Hybrid First Fit Packing"-Algorithmen. Als Maß für die Ähnlichkeit dienen gewichtete Formdifferenzen oder einfache kybernetische Shape-Matching-Algorithmen...

5/3K/4 (Item 4 from file: 348) [Links](#)

EUROPEAN PATENTS

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01047107

Traffic route finder in communications network

Verkehrswegesucher in einem Kommunikationsnetz

Chercheur de la route du trafic dans un reseau de communications

Patent Assignee:

• **Nortel Networks Limited;** (3029042)

2351 Boulevard Alfred-Nobel; St.Laurent, Quebec H4S 2A9; (CA)

(Proprietor designated states: all)

Inventor:

• **Mann, Jason Warren**

15 Gibbons Close; Borehamwood, Hertfordshire WD6 4TF; (GB)

• **White, Anthony Richard Phillip**

325 Ferndale Avenue; Ottawa, Ontario K12 6P9; (CA)

• **Turner, John Ian**

Home Cottage, Thriplow Road, Fowlmere; Royston, Hertfordshire SG8 7QT; (GB)

Legal Representative:

• **Anderson, Angela Mary et al (78507)**

Nortel Networks IP Law Group, Harlow Laboratories, London Road; Harlow, Essex CM17 9NA; (GB)

	Country	Number	Kind	Date	
Patent	EP	926860	A2	19990630	(Basic)
	EP	926860	A3	20010627	
	EP	926860	B1	20051102	
Application	EP	98309749		19981127	
Priorities	GB	9727163		19971224	

Designated States:

DE; FR; GB;

Extended Designated States:

AL; LT; LV; MK; RO; SI;

International Patent Class (V7): H04L-012/56 **Abstract Word Count:** 166**NOTE:** 4**NOTE:** Figure number on first page: 4

Type	Pub. Date	Kind	Text
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Publication: English

Procedural: English

Application: English

Available Text	Language	Update	Word Count
CLAIMS A	(English)	199926	867
SPEC A	(English)	199926	12713
CLAIMS B	(English)	200544	768
CLAIMS B	(German)	200544	746
CLAIMS B	(French)	200544	859
SPEC B	(English)	200544	12526
Total Word Count (Document A) 13583			
Total Word Count (Document B) 14899			
Total Word Count (All Documents) 28482			

Specification: ...generation process 604, route generation process 605, genetic manipulation process 606, population reproduction process 607, merge population process 608, fitness evaluation process 402, and **Steiner Tree** production process 610. The functions of each of the process are as follows. Engine control...Prim Kruskal algorithm. The minimum spanning tree algorithm connects the source node, destination nodes and **Steiner Vertices** together. In graph 1901 node n(underscore)6 only (shown hatched in Fig. 19) is a...

Specification: ...generation process 604, route generation process 605, genetic manipulation process 606, population reproduction process 607, merge population process 608, fitness evaluation process 402, and **Steiner Tree** production process 610. The functions of each of the process are as follows. Engine control...Prim Kruskal algorithm. The minimum spanning tree algorithm connects the source node, destination nodes and **Steiner Vertices** together. In graph 1901 node n(underscore)6 only (shown hatched in Fig. 19) is a...

5/3K/5 (Item 1 from file: 349) [Links](#)

PCT FULLTEXT

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01531225.

HIGH-SPEED SHAPE-BASED ROUTER

ROUTEUR ORIENTE FORME A GRANDE VITESSE

Patent Applicant/Patent Assignee:

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GB; GB (Residence); GB (Nationality)

(For all designated states except: US)

- **BIRCH Jeremy**; 8 Dugar Walk, Redland, Bristol BS6 7DH

GB; GB (Residence); GB (Nationality)

(Designated for all)

Patent Applicant/Inventor:

- **BIRCH Jeremy**

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Legal Representative:

- **HASELTINE LAKE(agent)**

Redcliff Quay, 120 Redcliff Street, Bristol BS1 6HU; GB;

	Country	Number	Kind	Date
Patent	WO	200774402	A2	20070705
Application	WO	2006IB3989		20060621
Priorities	US	2005595296		20050621

Designated States: (All protection types applied unless otherwise stated - for applications 2004+)

AE; AG; AL; AM; AT; AU; AZ; BA; BB; BG;
 BR; BW; BY; BZ; CA; CH; CN; CO; CR; CU;
 CZ; DE; DK; DM; DZ; EC; EE; EG; ES; FI;
 GB; GD; GE; GH; GM; HN; HR; HU; ID; IL;
 IN; IS; JP; KE; KG; KM; KN; KP; KR; KZ;
 LA; LC; LK; LR; LS; LT; LU; LV; LY; MA;
 MD; MG; MK; MN; MW; MX; MZ; NA; NG; NI;
 NO; NZ; OM; PG; PH; PL; PT; RO; RS; RU;
 SC; SD; SE; SG; SK; SL; SM; SY; TJ; TM;

TN; TR; TT; TZ; UA; UG; US; UZ; VC; VN;
 ZA; ZM; ZW;
 [EP] AT; BE; BG; CH; CY; CZ; DE; DK; EE; ES;
 FI; FR; GB; GR; HU; IE; IS; IT; LT; LU;
 LV; MC; NL; PL; PT; RO; SE; SI; SK; TR;
 [OA] BF; BJ; CF; CG; CI; CM; GA; GN; GQ; GW;
 ML; MR; NE; SN; TD; TG;
 [AP] BW; GH; GM; KE; LS; MW; MZ; NA; SD; SL;
 SZ; TZ; UG; ZM; ZW;
 [EA] AM; AZ; BY; KG; KZ; MD; RU; TJ; TM;

Publication Language: English
 Filing Language: English
 Fulltext word count: 11363

Detailed Description:

...is a method including: providing a layout with at least three pins to be coupled together using a net; adding a Steiner point to the layout; determining a path between the Steiner point and a first pin of...4 11, regarding route each part:
 [103] (a) For pin-to-pin or pin-to-Steiner-point joining sections that lie along a basis grid line (for instance are parallel to the X...

Claims:

...9. A method comprising: providing a layout with at least three pins to be coupled together using a net; adding a Steiner point to the layout; determining a path between the Steiner point and a first pin of... three segments.
 13. The method of claim 9 where the net coupling the three pins together will intersect at the Steiner point, or at as near a point as is possible.
 14. The method of claim 9... the routing problems in the order determined; and finding the net coupling the n pins together.
 18. The method of claim 17 where when a Steiner point is added, there will be at least n routing problems to be solved.
 19. The....

5/3K/6 (Item 2 from file: 349) [Links](#)

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01498318

RESILIENCY IN MINIMUM COST TREE-BASED VPLS ARCHITECTURE

RESILIENCE EN ARCHITECTURE VPLS A ARBORESCENCE DE COUT MINIMUM

Patent Applicant/Patent Assignee:

• **ALCATEL LUCENT**; 54, rue La Boetie, 75008 Paris

FR; FR (Residence); FR (Nationality)

(For all designated states except: US)

• **RAAHEMI Bijan**; 139 Riverdale Avenue, Ottawa, Ontario K1S 1R1

CA; CA (Residence); CA (Nationality)

• **BOU-DIAB Bashar Said**; 830-1845 Baseline Road, Ottawa, Ontario K2C 3K4

CA; CA (Residence); CA (Nationality)

• **CUERVO Fernando**; 3167 Woodkilton Road, R.R. #2, Ottawa, Ontario K0A 1T0

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Patent Applicant/Inventor:

• **RAAHEMI Bijan**

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• **BOU-DIAB Bashar Said**

830-1845 Baseline Road, Ottawa, Ontario K2C 3K4; CA; CA (Residence); CA (Nationality);

• **CUERVO Fernando**

3167 Woodkilton Road, R.R. #2, Ottawa, Ontario K0A 1T0; CA; CA (Residence); CA (Nationality);

Legal Representative:

• **HERVOUET Sylvie(agent)**

Feray Lenne Conseil, 39/41 avenue Aristide Briand, F-92163 Antony Cedex; FR;

	Country	Number	Kind	Date
Patent	WO	200739827	A2	20070412
Application	WO	2006IB3672		20060828
Priorities	US	2005212661		20050829

Designated States: (All protection types applied unless otherwise stated - for applications 2004+)

AE; AG; AL; AM; AT; AU; AZ; BA; BB; BG;
 BR; BW; BY; BZ; CA; CH; CN; CO; CR; CU;
 CZ; DE; DK; DM; DZ; EC; EE; EG; ES; FI;
 GB; GD; GE; GH; GM; HN; HR; HU; ID; IL;
 IN; IS; JP; KE; KG; KM; KN; KP; KR; KZ;
 LA; LC; LK; LR; LS; LT; LU; LV; LY; MA;
 MD; MG; MK; MN; MW; MX; MY; MZ; NA; NG;
 NI; NO; NZ; OM; PG; PH; PL; PT; RO; RS;
 RU; SC; SD; SE; SG; SK; SL; SM; SV; SY;
 TJ; TM; TN; TR; TT; TZ; UA; UG; US; UZ;
 VC; VN; ZA; ZM; ZW;
 [EP] AT; BE; BG; CH; CY; CZ; DE; DK; EE; ES;
 FI; FR; GB; GR; HU; IE; IS; IT; LT; LU;
 LV; MC; NL; PL; PT; RO; SE; SI; SK; TR;
 [OA] BF; BJ; CF; CG; CI; CM; GA; GN; GQ; GW;
 ML; MR; NE; SN; TD; TG;
 [AP] BW; GH; GM; KE; LS; MW; MZ; NA; SD; SL;
 SZ; TZ; UG; ZM; ZW;
 [EA] AM; AZ; BY; KG; KZ; MD; RU; TJ; TM;

Publication Language: English

Filing Language: English

Fulltext word count: 4867

Detailed Description:

...of branching. An alternate solution is provided herein based on computing a disjoint minimum-cost (Steiner) tree together with employing split horizon at the PE's as shown in Figure 2.

[0034] The...

5/3K/7 (Item 3 from file: 349) [Links](#).

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01076577

METHOD FOR AUTHENTICATION BETWEEN DEVICES

PROCEDE D'AUTHENTIFICATION ENTRE DISPOSITIFS

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(For all designated states except: US)

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(Designated only for: US)

- **TALSTRA Johan C**; c/o Prof. Holstlaan 6, NL-5656 AA Eindhoven

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(Designated only for: US)

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(Designated only for: US)

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(Designated only for: US)

Patent Applicant/Inventor:

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- **TALSTRA Johan C**

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c/o Prof. Holstlaan 6, NL-5656 AA Eindhoven; NL; NL(Residence); NL(Nationality); (Designated only for: US)

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Legal Representative:

- **GROENENDAAL Antonius W M(agent)**

Philips Intellectual Property & Standards, Prof. Holstlaan 6, NL-5656 AA Eindhoven; NL;

	Country	Number	Kind	Date
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Patent	WO	2003107589	A1	20031224
Application	WO	2003IB2340		20030527
Priorities	EP	200277423		20020617

Designated States: (All protection types applied unless otherwise stated - for applications 2004+)

[EP] AT; BE; BG; CH; CY; CZ; DE; DK; EE; ES;
 FI; FR; GB; GR; HU; IE; IT; LU; MC; NL;
 PT; RO; SE; SI; SK; TR;
 [OA] BF; BJ; CF; CG; CI; CM; GA; GN; GQ; GW;
 ML; MR; NE; SN; TD; TG;
 [AP] GH; GM; KE; LS; MW; MZ; SD; SL; SZ; TZ;
 UG; ZM; ZW;
 [EA] AM; AZ; BY; KG; KZ; MD; RU; TJ; TM;

Publication Language: English

Filing Language: English

Fulltext word count: 8313

Detailed Description:

...one of the revoked leaves upwards, to the root of the tree. The collection of merging paths is called the **Steiner Tree** ST(R) corresponding to leaves R. This is illustrated in Fig. 2, wherein a binary...

5/3K/8 (Item 4 from file: 349) [Links](#)

PCT FULLTEXT

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01076576

SYSTEM FOR AUTHENTICATION BETWEEN DEVICES USING GROUP CERTIFICATES

SYSTEME D'AUTHENTIFICATION ENTRE DISPOSITIFS UTILISANT DES CERTIFICATS DE GROUPE

Patent Applicant/Patent Assignee:

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NL; NL(Residence); NL(Nationality)

(For all designated states except: US)

• **LENOIR Petrus J**; c/o Prof. Holstlaan 6, NL-5656 AA Eindhoven

NL; NL(Residence); NL(Nationality)

(Designated only for: US)

• **TALSTRA Johan C**; c/o Prof. Holstlaan 6, NL-5656 AA Eindhoven

NL; NL(Residence); NL(Nationality)

(Designated only for: US)

• **VAN DEN HEUVEL Sebastiaan A F A**; c/o Prof. Holstlaan 6, NL-5656 AA Eindhoven

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(Designated only for: US)

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(Designated only for: US)

Patent Applicant/Inventor:

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• **TALSTRA Johan C**

c/o Prof. Holstlaan 6, NL-5656 AA Eindhoven; NL; NL(Residence); NL(Nationality); (Designated only for: US)

• **VAN DEN HEUVEL Sebastiaan A F A**

c/o Prof. Holstlaan 6, NL-5656 AA Eindhoven; NL; NL(Residence); NL(Nationality); (Designated only for: US)

• **STARING Antonius A M**

c/o Prof. Holstlaan 6, NL-5656 AA Eindhoven; NL; NL(Residence); NL(Nationality); (Designated only for: US)

Legal Representative:

• **GROENENDAAL Antonius W M(agent)**

Philips Intellectual Property & Standards, Prof. Holstlaan 6, NL-5656 AA Eindhoven; NL;

	Country	Number	Kind	Date
Patent	WO	2003107588	A1	20031224
Application	WO	2003IB2337		20030527
Priorities	EP	200277422		20020617

Designated States: (All protection types applied unless otherwise stated - for applications 2004+)

[EP] AT; BE; BG; CH; CY; CZ; DE; DK; EE; ES;
 FI; FR; GB; GR; HU; IE; IT; LU; MC; NL;

PT; RO; SE; SI; SK; TR;
 [OA] BF; BJ; CF; CG; CI; CM; GA; GN; GQ; GW;
 ML; MR; NE; SN; TD; TG;
 [AP] GH; GM; KE; LS; MW; MZ; SD; SL; SZ; TZ;
 UG; ZM; ZW;
 [EA] AM; AZ; BY; KG; KZ; MD; RU; TJ; TM;

Publication Language: English
 Filing Language: English
 Fulltext word count: 8918

Detailed Description:

...one of the revoked leaves upwards, to the root of the tree. The collection of merging paths is called the **Steiner Tree** ST(R) corresponding to leaves R. This is illustrated in Fig. 2, wherein a binary...

5/3K/9 (Item 5 from file: 349) [Links](#)

PCT FULLTEXT

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00912814

ROUTING METHOD AND APPARATUS

PROCEDE ET DISPOSITIF DE ROUTAGE

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US; US(Residence); US(Nationality)

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(Designated only for: US)

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(Designated only for: US)

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(Designated only for: US)

• **JACQUES Etienne;** 662 Garland Ave, Apt. #11, Sunnyvale, CA 94085

US; US(Residence); US(Nationality)

(Designated only for: US)

• **FRANKLE Jonathan A;** 17491 Hicks Rd., Los Gatos, CA 95032-6632

US; US(Residence); US(Nationality)

(Designated only for: US)

• **CHAO Heng-Yi;** 3301 Montecito Dr., San Jose, CA 95135

US; US(Residence); --(Nationality)

(Designated only for: US)

Patent Applicant/Inventor:

• **TEIG Steven**

935 College Ave., Menlo Park, CA 94025; US; US(Residence); US(Nationality); (Designated only for: US)

• **BUSET Oscar**

Rue de Lausanne 18, CH-1110 Morges; CH; CH(Residence); CA(Nationality); (Designated only for: US)

• **CALDWELL Andrew**

855 Monty Circle, Santa Clara, CA 95050; US; US(Residence); US(Nationality); (Designated only for: US)

• **JACQUES Etienne**

662 Garland Ave, Apt. #11, Sunnyvale, CA 94085; US; US(Residence); US(Nationality); (Designated only for: US)

• **FRANKLE Jonathan A**

17491 Hicks Rd., Los Gatos, CA 95032-6632; US; US(Residence); US(Nationality); (Designated only for: US)

• **CHAO Heng-Yi**

3301 Montecito Dr., San Jose, CA 95135; US; US(Residence); --(Nationality); (Designated only for: US)

Legal Representative:

• **ADELI Mani(agent)**

Stattler Johansen & Adeli LLP, P.O. Box 51860, Palo Alto, CA 94303-0728; US;

	Country	Number	Kind	Date
Patent	WO	200246975	A2-A3	20020613
Application	WO	2001US47016		20011206

Priorities	US	2000327567		20001207
	US	2001325748		20010119
	US	2001314580		20010823

Designated States: (All protection types applied unless otherwise stated - for applications 2004+)

[EP] AT; BE; CH; CY; DE; DK; ES; FI; FR; GB;
GR; IE; IT; LU; MC; NL; PT; SE; TR;
[OA] BF; BJ; CF; CG; CI; CM; GA; GN; GQ; GW;
ML; MR; NE; SN; TD; TG;
[AP] GH; GM; KE; LS; MW; MZ; SD; SL; SZ; TZ;
UG; ZM; ZW;
[EA] AM; AZ; BY; KG; KZ; MD; RU; TJ; TM;

Publication Language: English

Filing Language: English

Fulltext word count: 47933

Detailed Description:

...and 28 respectively illustrate examples of path-usage counts and path-usage probabilities for the Steiner trees of Figures 6

Figure 29 illustrates a compression technique for storing Steiner-tree routes for sets of net configurations.

Figures 30 and 31 illustrate one technique for grouping ...strings by using the 16-bit configuration code of the net configuration

0000001000000001.

4* Storing Steiner Trees in a Compressed Form

10 A variety of compression techniques can be employed to store and use Steinertree routes for sets of net configurations...stored bit string. After 3335 or 3340, the process ends.

C. Identifying Routes From the Compressed Pre-Tabulated Table

When the Steiner-tree routes are pre-tabulated according to process 2900, a router at ran-time identifies one...

5/3K/10 (Item 6 from file: 349) [Links](#)

PCT FULLTEXT

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00560547

APPROACH FOR ROUTING AN INTEGRATED CIRCUIT

PROCEDE DE ROUTAGE D'UN CIRCUIT INTEGRE

Patent Applicant/Patent Assignee:

- CHAPMAN David C;

::

	Country	Number	Kind	Date
Patent	WO	200023920	A1	20000427
Application	WO	99US24454		19991019
Priorities	US	98104872		19981019
	US	99139532		19990616

Designated States: (All protection types applied unless otherwise stated - for applications 2004+)

Publication Language: English

Filing Language:

Fulltext word count: 23269

Detailed Description:

...integrated circuit is represented by a bipartite graph, in which nodes representing connections (pins or Steiner Points) are joined by an arbitrary number of edges. The connections are referred to herein as "join points..."

5/3K/11 (Item 7 from file: 349) [Links](#)

PCT FULLTEXT

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00468850

SYSTEM AND METHOD FOR ASYNCHRONOUS, ADAPTIVE MOVING PICTURE COMPRESSION, AND DECOMPRESSION

SYSTEME ET PROCEDE DE COMPRESSION ET DE DECOMPRESSION D'IMAGES ANIMEES ASYNCHRONES ET ADAPTATIFS

Patent Applicant/Patent Assignee:

- REAL-TIME GEOMETRY CORPORATION;

::

	Country	Number	Kind	Date
Patent	WO	9859315	A1	19981230
Application	WO	98US13056		19980623
Priorities	US	97880806		19970623

Designated States: (All protection types applied unless otherwise stated - for applications 2004+)

Publication Language: English

Filing Language:

Fulltext word count: 23511

Detailed Description:

...2). The mesh model 34 is configured exactly as the mesh model 20 of the **compression** function 16 (e.g., initially with the two triangle **Steiner point** construction described in Fig. 3) and is prepared to accept the addition and deletion of...

5/3K/12 (Item 8 from file: 349) [Links](#)

PCT FULLTEXT

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00356447

METHOD AND TRANSMISSION SYSTEM RELATED TO MULTICASTING

PROCEDE ET SYSTEME DE TRANSMISSION CONCERNANT LA MULTI-DIFFUSION

Patent Applicant/Patent Assignee:

• **TELIA AB;**

::

• **KAVAK Nail;**

::

	Country	Number	Kind	Date
Patent	WO	9638961	A1	19961205
Application	WO	96SE682		19960528
Priorities	SE	951999		19950531

Designated States: (All protection types applied unless otherwise stated - for applications 2004+)

Publication Language: English

Filing Language:

Fulltext word count: 6307

Claims:

...a c t e r i s e d i n

that said paths constitute a **Steiner tree**.

14 A packet switched data transmission system, having a plurality of switching nodes, each serving a plurality of...

?